

Amendments to the Specification:

Please replace the following paragraph 26 with the following amended paragraph (in which deleted matter is shown with strikethrough text (e.g., ~~strikethrough~~) and in which added matter is shown underlined):

[0026] The RB_X resistors of the resistor ladder 201 are adjustable to enable selection of intermediate tap points. In one embodiment, each RB_X resistor is an adjustable tap resistor having an adjustable tap point to adjust the relative tap voltage. In the embodiments described herein, the adjustable taps (for either or both resistors RB_X and RA_X) are implemented using multiple series-coupled resistors and switch logic to select discrete intermediate junctions. The adjustable tap resistors may alternatively be referred to as potentiometers, which have a constant total resistance and an adjustable intermediate tap point. For the resistor ladder 201, each of the RB_X resistors is further sub-divided as illustrated by an exploded view of the first resistor RB_1 . The resistor RB_1 is further sub-divided into a series-connected string of P resistors $RB1_1$, $RB1_2$, ..., $RB1_P$, where “ P ” is another positive integer. The number P is arbitrary and is based on the level of tap point granularity desired for a given ~~impleentation~~ implementation. Although not shown, each of the remaining RB_X resistors RB_2 , RB_3 , ..., RB_M are sub-divided in a similar manner. Select logic is coupled to the intermediate junctions of each RB_X resistor, where the select logic selects one of the intermediate junctions as a tap point selected for a channel voltage to be provided on a corresponding one of the signal lines 109. As shown, select logic SL1 includes $P-1$ switches S_1 , S_2 , ..., S_{P-1} , each coupled to a corresponding intermediate junction of the resistor string $RB1_1 - RB1_P$.